

## dbx: SC 32/64 Output Mono-Mixer

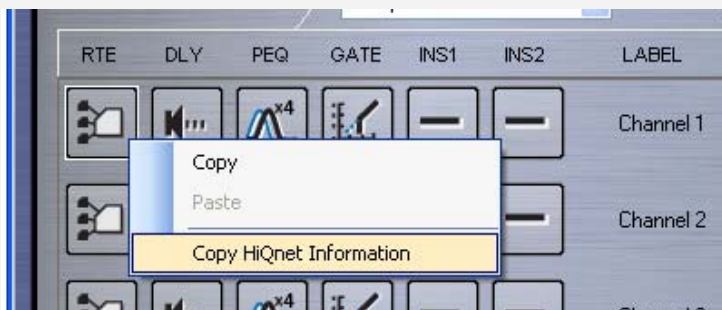
This module provides functionality for an Output Mono-Mixer.



### GENERAL INFORMATION

<b>SIMPLWINDOWS NAME:</b>	dbx SC32-64 Output Mono-Mixer.umc
<b>CATEGORY:</b>	Device Interface
<b>VERSION:</b>	v1.0
<b>SUMMARY:</b>	This module provides functionality for a single Output Mono-Mixer. This module <b>MUST</b> connect to a "dbx SC32-64 NODE.umc". <b>DO NOT CONNECT DIRECTLY TO A COMM PORT.</b>
<b>GENERAL NOTES:</b>	<b>This module was written by the Manufacturer.</b>

This module is meant for a single dbx SC32/64 Output Mono-Mixer. Use a different instance for each SC 32/64 Output Mono-Mixer being controlled. The user must select the VD and ObjectID via the parameter drop-down list. This address information can be obtained by right-clicking on the desired object and selecting "Copy HiQnet Information".



Name(type of object): Router  
Node: (Hex):0x01, (Decimal):1  
VD: (Hex):0x01, (Decimal):1  
ObjectID: (Hex):0x010100, (Decimal):1.1.0

Extensive debug capabilities have been built into this module. To access additional information concerning errors, messages types/values, module status, and basic trouble shooting information the programmer can uncomment the #DEFINE statements in the Simpl+ code. NOTE: debugging info takes processor resources. Comment out the #DEFINE for final release to ensure optimum performance.

The SC is user configurable and thus the programmer can determine how much traffic and how much control is exchanged between the Crestron and the SC by using external sub-modules such as "dbx SC32-64 Output Mono-Mixer.umc" available in the module download package. To utilize additional modules the programmer must expand the "From\_NODE\_Module" and "To\_NODE\_Module" signals. ONLY 1 sub-module can be tied to each signal on the NODE. See sample image on last page of this document.

## CONTROL:

<b>Device_Ready</b>	D	After communication has been established to the SC, the NODE will assert this signal allowing all sub-modules to send and receive data from the SC. This signal must be connected to the NODE module's "Device_Ready" signal output for proper functionality. No messages will be sent from this module unless this signal is HIGH.
<b>Subscribe</b>	D	<p>This signal is intended to be HIGH for all typical system usages. It must be HIGH to receive true feedback from the SC. If it is low, the feedback is simulated.</p> <p>When this signal transitions from low to high 'subscribe' messages will be sent to the SC which will allow automatic updates to be received (no polling). When this signal transitions from high to low 'unsubscribe' messages will be sent to the SC and automatic updates will not be received any longer.</p> <p>It is strongly suggested that the programmer connect this input to the same signal as defined on the DEVICE_READY input. This will ensure that this module stays in sync during connection interruptions. See last page of this document for proper connections.</p>
<b>Discrete_Volume_Enable</b>	D	<p>This signal is intended to be LOW for most applications. If the "Subscribe" line is high the Crestron module will be kept in sync automatically.</p> <p>If this signal is HIGH a "GET" message will follow all volume "SET" commands. This is only for user's who want a full response for every single volume command. NOTE: This can cause high traffic/processor loads.</p>
<b>Master_Mute</b>	D	When this signal transitions low-to-high a "Mute" message will be sent to the SC. When the signal transitions from high-to-low an "UnMute" message will be sent to the SC.
<b>Input_Mute_[x]_Enable</b>	D	<p>Array of digital inputs. Expand this array to expose the necessary signals.</p> <p>When this signal transitions low-to-high a "Mute" message will be sent to the SC. When the signal transitions from high-to-low an "UnMute" message will be sent to the SC.</p>
<b>Master_Volume</b>	A	Accepts values of 0-65535 so that this signal input can be connected directly to a Analog_Ramp. This module is designed so that the SAME SIGNAL name that is defined on the Master_Volume_In input can be defined on the Master_Volume_FB in order to avoid logic buffering when using an Analog_Ramp.
<b>From_NODE_Module</b>	S	<p>Connect the "To_IO_Module" signal from the dbx NODE module to this input. Data received from the device will be sent to this module via the NODE. DO NOT CONNECT DIRECTLY TO A COMM PORT.</p> <p><b>IMPORTANT:</b> Only 1 module may be connected to each "From_NODE_Module" input. If more than 1 module is connected the modules WILL NOT FUNCTION properly. See example screenshot on the last page of this document for proper connection.</p>

<b>Input_[x]_Volume</b>	A	<p>Array of analog inputs.</p> <p>Accepts values of 0-65535 so that this signal input can be connected directly to a Analog_Ramp. This module is designed so that the SAME SIGNAL name that is defined on the Input_[x]_Volume input can be defined on the Input_[x]_Volume_Graph in order to avoid logic buffering when using an Analog_Ramp.</p>
<b>Input_[x]_Source</b>	A	<p>Array of analog inputs.</p> <p>Will select the source of the input channel. Valid values are 0-32d.</p>

## FEEDBACK:

<b>Master_Mute_Is_On</b>	D	<p>Will update the current state of the MasterMute parameter. If "subscribe" is HIGH the feedback will be true. If "subscribe" is LOW the feedback will be pseudo.</p>
<b>Input_Mute_[x]_Is_On</b>	D	<p>Array of digital outputs.</p> <p>Will update the current state of the Input_Mute_[x]_Is_On parameter. If "subscribe" is HIGH the feedback will be true. If "subscribe" is LOW the feedback will be pseudo.</p>
<b>To_NODE_Module</b>	S	<p>Connect to the "From_Module" signal on the dbx NODE module. DO NOT CONNECT DIRECTLY TO A COMM PORT.</p> <p><b>IMPORTANT:</b> Only 1 module may be connected to each "To_NODE_Module" output. If more than 1 module is connected the modules WILL NOT FUNCTION properly. See example screenshot on the last page of this document for proper connection.</p>
<b>Master_Volume_Graph</b>	A	<p>Will reflect the current state of the Master Volume.</p> <p>This module is designed so that the SAME SIGNAL name that is defined on the "Master_Volume_In" input can be defined on the "Master_Volume_Graph" in order to avoid logic buffering when using an Analog_Ramp.</p> <p>If "Discrete_Volume_Enable" is HIGH this signal name must be different from "Master_Volume_In".</p>
<b>Input_[x]_Volume_Graph</b>	A	<p>Array of analog outputs. Expand as necessary. Will reflect the current state of the Input_[x]_Volume channel.</p> <p>This module is designed so that the SAME SIGNAL name that is defined on the "Input_[x]_Volume_In" input can be defined on the "Input_[x]_Volume_Graph" in order to avoid logic buffering when using an Analog_Ramp.</p> <p>If "Discrete_Volume_Enable" is HIGH this signal name must be different from "Input_[x]_Volume_In".</p>

**Input\_[x]\_Source\_Status**

A

Array of analog outputs. Will reflect the current state of the Input\_[x]\_Source channel.

Will reflect the current state of the Source of the channel. If "subscribe" is HIGH the feedback will be true. If "subscribe" is LOW the feedback will be pseudo. Normally this signal is sent to an Analog Equate for true feedback purposes. See example program.

**Parameters:****VD**

The user must select the VD and ObjectID via the parameter drop-down list. This address information can be obtained by right-clicking on the desired object and selecting "Copy HiQnet Information". See GENERAL NOTES at top of this document for more information.

NOTE: A valid correlation must be met between the selected VD and ObjectID parameters. If this is not met this module **will not function**. A constant "Configuration Mismatch" Error will be displayed in the Crestron console and Simpl Debugger until this condition has been resolved. An SC processor need not be present for this condition to be detected.

**ObjectID**

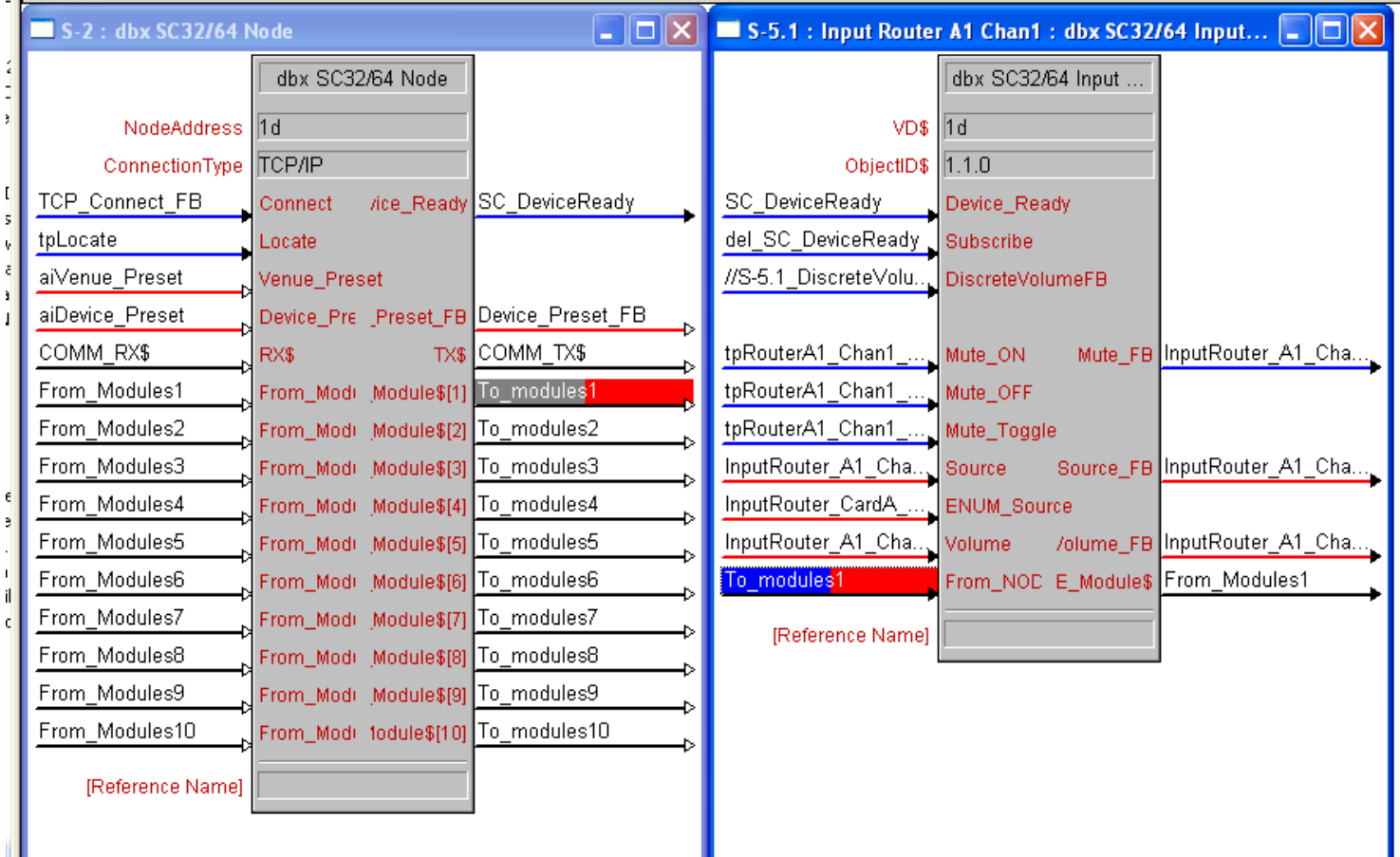
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Sample Screen shot of NODE module and Input Router sub-module defined properly.



## Detail View



**OPS USED FOR TESTING:**

**Firmware:** PRO2 4.001.1012

**COMPILER USED FOR TESTING:**

V2.12.18; Simpl+ 3.03.11

**SAMPLE PROGRAM:**

dbx SC32-64 Demo Program.smw and dbx SC32-64 TPMC-12 Demo Panel.vtp

**REVISION HISTORY:**

V. 1.0 – Creation by Manufacturer (S.E.)