

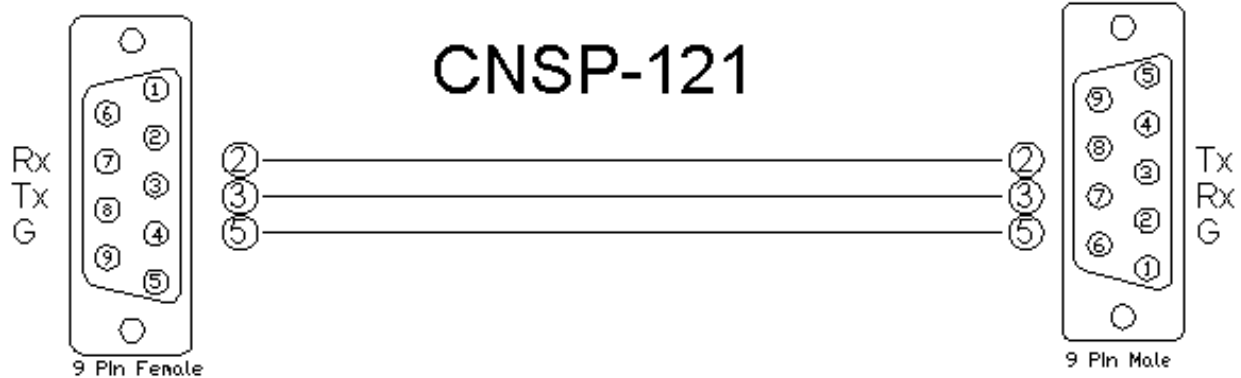
Partner: Algorith
 Model: Mosquito
 Device Type: Video Processor



GENERAL INFORMATION

SIMPLWINDOWS NAME:	Algorith Mosquito V1.0
CATEGORY:	Video Processor
VERSION:	1.0
SUMMARY:	Controls all standard functions on Algorith Mosquito video processing system. Provides true feedback.
GENERAL NOTES:	<p>This module will control an Algorith Mosquito.</p> <p>Note: Manufacturer does not provide Auto status feedback.</p> <p>You may pulse Poll_Settings once at boot time to get the serial number and revision strings populated. You may hold Poll_Enable high to poll for device status every 1 second. This is typically done on demand (when visiting control pages for this device). The Busy_Initializing_Fb shows the state of video processor when it is being polled for its settings (once at boot time, and any time power is cycled), which is a long (over 15 second) sequence. During the time of initialization, the Mosquito's processor ignores new commands. Since the Mosquito does not have any internal buffering scheme, this module provides serial queuing of commands, with 5 repeated sends on any failed compared requested value.</p> <p>Note that all inputs to this module should be momentary, as if coming from a button on a touch panel.</p>
CRESTRON HARDWARE REQUIRED:	CNCOMH-2, CNXCOM, ST-COM, C2COM
SETUP OF CRESTRON HARDWARE:	<p>RS232</p> <p>Baud: 38,400</p> <p>Parity: N</p> <p>Data Bits: 8</p> <p>Stop Bits: 1</p>
VENDOR FIRMWARE:	<p>Global Software Version: 2.06</p> <p>Document Revision: 3023-6003DS-05_Serial_Option_Protocol_Design_Specification at August 27, 2007.</p>
VENDOR SETUP:	<p>Must plug in unit without serial option first, and then plug in USB to RS232 serial option. Note: A reset Serial Option command must be sent after the reset command. Allow a delay of 10 seconds for the completion of the reset.</p>
CABLE DIAGRAM:	CNSP -121

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CONTROL:

Power_On/Off/Toggle	D	Pulse to turn the unit on or off.
Input_*	D	Pulse to pick a source input (HDMI 1-4 or Component 1-2).
Display_Content_Normal_Mode	D	Pulse to display content normal mode.
Display_Content_Map_Detection	D	Pulse to display content map detection mode.
Split_Screen_Normal_Mode	D	Pulse to display screen normal mode.
Split_Screen_Split_Mode	D	Pulse to display screen split mode.
Process_On/Off	D	Pulse to switch video processing on/off.
Mode_2/3D_*	D	Pulse to switch video processing 2D and 3D modes.
MNR_Mode	D	Pulse to set MNR mode.
DNR_Mode	D	Pulse to set DNR mode.
BAR_*	D	Pulse to set Bar mode.
Detail_Enhancement_*	D	Pulse to set Detail enhancement level.
DRS_On/Off	D	Pulse to switch video DRS on/off.
Save_Input_Settings_*_SD	D	Pulse to save current input settings into a slot for SD.

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Save_Input_Settings_*_HD	D	Pulse to save current input settings into a slot for HD.
Poll_Settings	D	Pulse (usually at boot time) to enable polling of unit for it's not often changed settings like Serial Number, Revision, etc.
Poll_Enable	D	Hold high to enable polling of unit for it's input and processing settings every 1 second. Hold low to disable polling when not visiting pages directly related to control of the Video Processor.
From_Device\$	S	Serial signal to be routed from a 2-way RS232 port.

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FEEDBACK:

Power_On/Off_Fb	D	True feedback indicating the power state.
Input_Fb	D	True feedback indicating the input state.
Display_Content_*_Fb	D	True feedback indicating the display content state.
Split_Screen_*_Fb	D	True feedback indicating the split screen state.
Processing_On/Off_Fb	D	True feedback indicating the video processing state.
MNR_Fb	D	True feedback indicating the MNR processing state.
DNR_Fb	D	True feedback indicating the DNR processing state.
BAR_*_Fb	D	True feedback indicating the BAR processing state.
Detail_Enhancement_*_Fb	D	True feedback indicating the detail enhancement level.
DRS_On/Off_Fb	D	True feedback indicating the DRS On/Off states.
LED_intensity_On/Off_Fb	D	True feedback indicating the LED intensity state.
English/French_Language_Fb	D	True feedback indicating the current language displayed.
Test_Pattern_No_Test_Fb	D	True feedback indicating the current test pattern is no test displayed.
Test_Pattern_Matrix_Fb	D	True feedback indicating the current test pattern is matrix displayed.
Test_Pattern_Bar_Fb	D	True feedback indicating the current test pattern is bars displayed.

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Temporal_Filter_*_Fb	D	True feedback indicating the current temporal filter state.
Spatial_Filter_0_Fb	D	True feedback indicating the current spatial filter state.
Resolution_*_Fb	D	True feedback indicating the current resolution state.
FPGA_Revision\$	S	Serial string data indicating the FPGA Revision in ASCII.
CPLD_Revision\$	S	Serial string data indicating the CPLD Revision in ASCII.
MicroController_Revision\$	S	Serial string data indicating the Microchip Revision in ASCII.
Serial_Number_Part_1\$	S	Serial string data indicating the Serial Number Part 1 in ASCII.
Serial_Number_Part_2\$	S	Serial string data indicating the Serial Number Part 2 in ASCII.
PCB_Serial_Number\$	S	Serial string data indicating the PCB Serial Number in ASCII.
Date_Code\$	S	Serial string data indicating the Date of Manufacture in ASCII.
Global_Revision\$	S	Serial string data indicating the global revision number in ASCII.
Busy_Initializing_Fb	D	Shows state of video processor when it is being polled for its settings (once at boot time, and any time power is cycled), which is a long (over 15 second) sequence. During the time of initialization, the processor ignores new commands.
To_Device\$	S	Serial signal to be routed to a 2 way RS232 port.

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**TESTING:**

OPS USED FOR TESTING:	PRO2 Cntrl Eng {v3.137 (Release)}
SIMPL WINDOWS USED FOR TESTING:	V. 2.08.38
CRES DB USED FOR TESTING:	V. 18.7.8
SYMBOL LIBRARY USED FOR TESTING:	V.472
SAMPLE PROGRAM:	Algorith Mosquito Demo V1.0.smw
REVISION HISTORY:	V. 1.0