



Partner: QSC Q-SYS
Model: Q-SYS Cores
Device Type: DSP

GENERAL INFORMATION

SIMPLWINDOWS NAME:	Q-SYS [Button] V4.2
CATEGORY:	Q-SYS V4
VERSION:	V4.2
SUMMARY:	This module is used to control a button type object.
GENERAL NOTES:	This module is used to control a Q-SYS named control object, as defined in the Q-SYS software. It is to be used in partner with one Q-SYS Core module with the same Core ID parameter. This module can be used to control the state of a button within the Q-SYS design.
CRESTRON HARDWARE REQUIRED:	Ethernet Card – 3-Series Only
SETUP OF CRESTRON HARDWARE:	3-Series Only
VENDOR FIRMWARE:	Unknown
VENDOR SETUP:	Setup of Q-SYS Design file, “Named Controls” of desired components for control.

CONTROL:

Initialize	D	Hold to initialize, and register this module to its respective Core module. Usually driven from the “Initialized” signal of the Core module.
[Button_State_On]	D	Optional – Rising Edge – Sets state of button object high/on
[Button_State_Off]	D	Optional – Rising Edge – Sets state of button object low/off
[Button_State_Tgl]	D	Optional – Rising Edge – Toggles the state of the button object
[Poll_Enable]	D	Optional – Hold high to add object to Core polling group, object will be “polled” at the interval defined by the Core module. Recommend to use only when viewing/needng this value.



Partner: QSC Q-SYS
Model: Q-SYS Cores
Device Type: DSP

FEEDBACK:

[Error]	D	(Future Use) – Place Holder
[Button_State_On_FB]	D	Optional –Indicates button object is high/on
[Button_State_Off_FB]	D	Optional –Indicates button object is low/off

PARAMETERS:

Core ID	Dec	Used to register this module to its respective Core Module.
Named Control	S	String to identify the object in Q-SYS design to control. Setup in the Q-SYS design software.

TESTING:

OPS USED FOR TESTING:	v4.001.1012
SIMPL WINDOWS USED FOR TESTING:	4.07.03
DEVICE DB USED FOR TESTING:	85.05.003.00
CRES DB USED FOR TESTING:	63.05.007.0
SYMBOL LIBRARY USED FOR TESTING:	1035
SAMPLE PROGRAM:	Q-SYS V4.0 Demo Program
REVISION HISTORY:	V4.0 – Completely Revamped Module Set. V4.2 – Fixed Initialization Issue, More Efficient