

Partner: GE Interlogix
Model: NX-8E
Device Type: Security



GENERAL INFORMATION

SIMPLWINDOWS NAME:	GE Interlogix Networx View Events v4.7
CATEGORY:	Security
VERSION:	4.7
SUMMARY:	This module allows viewing of the event log.
GENERAL NOTES:	This module allows the user to view the event log.
CRESTRON HARDWARE REQUIRED:	C2COM1, C2COM2/3
SETUP OF CRESTRON HARDWARE:	RS232 Baud: 9600 Parity: None Data Bits: 8 Stop Bits: 1
VENDOR FIRMWARE:	NX-8E V19.00 65BD 08/03/07

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VENDOR SETUP:

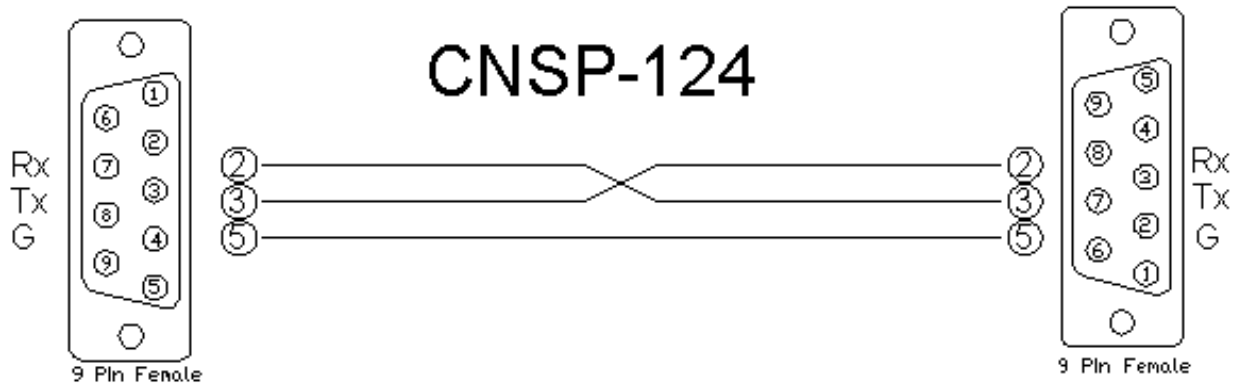
The NX-8E has the NX-584 built onto the main board. You must enter programming mode and enable the NX-584 by setting Location 207 to "1". Location 23 enables and disables function globally. For instance, if Location 23 Segment 1 Bit 1 is enabled, the STAY function will be enabled on the NX-8E keypads. The STAY function will be enabled on the Crestron system if Location 23 Segment 1 Bit 1 and Location 211 Segment 4 Bit 7 are enabled. If Location 23 Segment 1 Bit 1 is disabled, the STAY function will be disabled for both the NX-8E keypads and the Crestron system, no matter what Location 211 Segment 4 Bit 7 is set to.

The following locations need to be set as listed below.

Location	Setting
23	Segment 1 bits 1, 5, 6 & 7 enabled. All others disabled.
23	Segment 2 bit 4 enabled. All others disabled.
23	Segments 3, 4 & 5 all bits disabled.
207	"1" for NX-584 Enabled.
208	"2" for 9600 Baud.
209	Bit 1 set to "1" for LED On ASCII.
210	Segment 1 All disabled.
210	Segment 2 All disabled.
211	Segment 1 bits 4, 6 & 7 enabled. All others disabled.
211	Segment 2 bits 1 & 3 enabled. All others disabled.
211	Segment 3 bits 3, 5 & 7 enabled. All others disabled.
211	Segment 4 bits 5, 7 & 8 enabled. All others disabled.

CABLE DIAGRAM:

CNSP-124



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**CONTROL:**

Show_Log	D	Pulse to show the first page of the log.
Previous_Page	D	Pulse to show the previous page of the log.
Next_Page	D	Pulse to show the next page of the log.
From_Processor_Module\$	S	Serial signal to be routed from the GE Interlogix Networx Processor Module v4.7.

PARAMETER:

Number of Events per Page	P	Number of events to show per page.
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FEEDBACK:

Display_Event_*\$	S	Serial signal with the text of the event.
To_Processor_Module\$	S	Serial signal to be routed to the GE Interlogix Networx Processor Module v4.7.

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**TESTING:**

OPS USED FOR TESTING:	CP3: 1.501.0013
SIMPL WINDOWS USED FOR TESTING:	4.03.20
DEVICE DB USED FOR TESTING:	72.00.001.00
CRES DB USED FOR TESTING:	54.05.005.00
SYMBOL LIBRARY USED FOR TESTING:	982
SAMPLE PROGRAM:	GE Interlogix Networx v4.7 Demo

REVISION HISTORY:

2.0 – 7/27/2005 – Changed several modules. The processor module has been changed so that it does not poll the NX-8E. This allows the commands to be sent to the NX-8E more promptly. The Partition and Zone modules have been changed to provide more feedback. All SIMPL+ modules have been changed to use volatile memory instead of non-volatile memory.

3.0 – 9/22/2005 – Changed several modules. The processor module has been changed so that it does poll. This allows us to control all communications between the Crestron and the NX-8E. The zone bypass modules and the zone name modules have been changed to allow the zone number to be entered as a decimal. This will allow the module to be copied and pasted using the auto increment function.

4.0 – 5/17/2006 – Fixed the GE Interlogix Networx Processor Module v4.0 module. It had a user function that had the same name as a new built in function in the Simpl+ file.

4.1 – 1/27/2009 – Fixed an issue with the GE Interlogix Networx Processor Module v4.1 that caused errors in the processor module. Also fixed a labeling issue with the cable diagram in the help file.

4.2 – 8/7/2009 – Fixed an issue with processing the responses from the GE causing errors. Changed the way that the serial queue is handled. Increased the size of the command queue.

4.3 – 1/6/2010 – Per GE, changed the poll time to 15 seconds.

4.6 – Optimized the Simpl+ for 3-series processors.

4.7 – Fixed an issue with the GE Interlogix Networx Processor Module not properly handling the feedback responses for the partitions.