HELP FILE DOCUMENT

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GENERAL INFORMATION

SIMPLWINDOWS NAME:	JSON FileProcessor v2.1.0
CATEGORY:	Utility
VERSION:	2.1.0
SUMMARY:	Module provides a method by which to arbitrarily parse JSON tokens from a file loaded to processor memory and to write stored values to the same file.
GENERAL NOTES:	Each Json file to be parsed must have its own FileProcessor module with a unique Processor ID and a local path to the Json file. Each signal type has a module that may be utilized to parse Json Properties and to assign them to a SimplWindows signal type. Each module can parse up to 10 tokens, but you may have multiple instances of each module type. Parsing modules associated with a FileProcessor module must have matching Processor ID parameters. Please be aware that the reader module takes time to register all requester and writer modules. To this end, read and write requests are buffered until all requester and writer modules have been registered. This usually takes 5-15 seconds. If you do not place a json file on the processor corresponding to the json file path, a blank file will be created on first read or first write. To learn more about what other utility modules are available from Control Concepts visit the CCI Utility Module Store.
CRESTRON HARDWARE REQUIRED:	3-Series, 4-Series or VC-4 processors **ONLY**.
SETUP OF CRESTRON HARDWARE:	N/A
VENDOR FIRMWARE:	N/A
VENDOR SETUP:	N/A
CABLE DIAGRAM:	N/A

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File Path Information

The module has a parameter called “Json File” which is intended to contain the path on the processor to the JSON file to use for reading/writing. This path can be set up/named in the module parameter the same no matter whether using a 3-series, 4-series of VC-4 processor and the module will automatically handle the entered path as necessary on the platform it is running on. The module will also automatically convert the path separators to work on the platform the module is running on.

As an example, suppose you enter “\\USER\\config.json” in this parameter field.

On a 3-series or 4-series processor, the module will read from/write to a JSON file named “config.json” located in the global “User” folder on the processor.

On a VC-4 system, each running program will have its own local filesystem. If, for example, you’re running the module on a VC-4 server in a room with the ID of “TESTROOM”. In this case, the example above would be located at the following path:

“/opt/crestron/virtualcontrol/RunningPrograms/TESTROOM/User/config.json”

The first part of the path (everything up to TESTROOM) will be automatically determined by the Room ID the program is running in. The second part of the path (everything after TESTROOM) will be determined by what you enter in the “Json File” parameter on the module.

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Control:

Signal/Function Name	*D,S,A*	*Digital, Serial, Analog signal property definition.*
Read_File	D	Triggers the file read and parse operation on the rising edge. Placing a '1' on this signal is valid, however not encouraged, as the parsing operation is fairly processor intensive. When combined with other processor intensive on-boot tasks in the program, it may cause sluggish operation until the parsing action has concluded.
Write File	D	Triggers the file write operation on the rising edge. Placing a '1' on this signal is not valid. Values will only be written if they have already been stored by a corresponding 'Writer' module.
Debug	D	Enables debugging trace statements for all associated 'Readers' and 'Writers' when high.

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PARAMETERS:

Json File	S	Path to the JSON file that you wish to poll. Remember to use the escape character (\) as necessary in file paths.
Processor ID	A	Setting to indicate the ID for a specific Processor Module. Each 'Reader' and 'Writer' must be paired with an appropriate Processor via matching Processor ID fields.

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TESTING:

OPS USED FOR TESTING:	CP3: 1.8001.4925.26115 CP4: 2.8000.00017 VC-4: 4.0000.00007
SIMPL WINDOWS USED FOR TESTING:	4.22
DEVICE DB USED FOR TESTING:	200.250
CRES DB USED FOR TESTING:	217.05
SYMBOL LIBRARY USED FOR TESTING:	1181
REVISION HISTORY:	1.0.0 – Initial Release 1.0.1 – Fix read issue in .clz affecting 4-series processors 1.1.0 – Add support for VC-4 and simplify demo program 2.0.0 – Upgraded to 2.0 Library 2.1.0 – Creates file and writes paths to file if they do not exist.

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